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APPLICANTS

Michael Hubner, Schonau, GERMANY;

Gunnar Krause, Munchen, GERMANY;
 Justus Kuhn, Munchen, GERMANY; Jochen Muller, Munchen, GERMANY;
 Peter Pochmuller, Colchester, VT;
 Jurgen Weidenhofer, Munchen, GERMANY;

** CONTINUING DATA *****

This application is a DIV of 10/010,504 12/05/2001 PAT 6,762,611

** FOREIGN APPLICATIONS *****

GERMANY 100 60 438.2 12/05/2000

*E. ZK.**E. ZK.*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	GERMANY	DRAWING 4	CLAIMS 6	CLAIMS 1
Verified and Acknowledged	<i>E. ZK.</i> Examiner's Signature	Initials			

ADDRESS

LAURENCE A. GREENBER
 P.O. Box 2480
 Hollywood, FL
 33022

TITLE

Method and probe card configuration for testing a plurality of integrated circuits in parallel

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